

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Satoshi YAMANAKA et al.

Application No.: 10/541,611

Confirmation No.: 8340

Filed: July 7, 2005

Art Unit: 2625

For: PIXEL INTERPOLATION CIRCUIT, PIXEL
INTERPOLATION METHOD AND IMAGE
SCANNER

Examiner: R. Z. Zhu

AMENDMENT AFTER FINAL ACTION UNDER 37 C.F.R. 1.116

MS AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

INTRODUCTORY COMMENTS

In response to the Office Action dated December 30, 2009, please amend the above-identified U.S. patent application as follows:

Remarks/Arguments